Features:

- Built-in power clamp, support input voltage 5~24V
 Summarize
- OUT R/G/B/W constant curren default 17mA
- OUT R/G/B/W Power-on status: No light
- OUT R/G/B/W port withstand voltage 26V
- OUT R/G/B/W output gray level: 256 levels
- Data synchronization refresh in the same frame
- Data serial transmission by single |
- Unipolar return-to-zero code data protocol

• Built in signal reshaping circuit, to ensure waveform distortion do not accumulate after wave reshaping to the next driver

- Send data at speed of 800Kbps.
- SOP8 Package

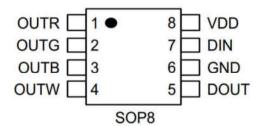
The SM16704 is 4 output channels control IC that special for LED driver circuit.Its internal includes power clamp module, signal decoding module Block, oscillation module, data regeneration module, output current drive module, etc.

IC use single ZR communication mode and adopt auto reshaping transmit technology, ensuring that the data is not fading during serial transmission.the DIN port receive data from controller, the first IC collect initial 32bit data then sent to the internal data latch, the other data which reshaping by the internal signal reshaping amplification circuit sent to the next cascade IC through the DO port.

Application field

- •Building exterior / scene lighting
- Indoor&outdoor LED decorative lighting
- Pixel led lighting
- Flexible led strip, linear led lighting

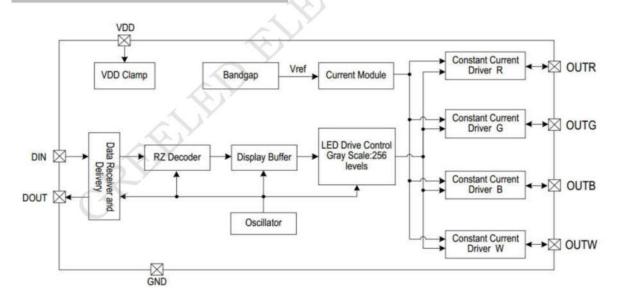
Footprint



PIN Define

NO.	Symbol	Function description
1	OUTR	Output of RED PWM control
2	OUTG	Output of GREEN PWM control
3	OUTB	Output of BLUE PWM control
4	OUTW	Output of White PWM control
5	DOUT	Data Output
6	GND	Data & Power Grounding
7	DIN	Control data input
8	VDD	IC power supply

IC internal functional block diagram



Parameter	Ratings	Unit
Power Supply Voltage	-0.4~5.5	v
Logical Input Voltage	-0.4~VDD+0.4	v
R/G/B Channel Output Port Withstand Voltage	30	v
Max clamp current	20	mA
Thermal Resistance	130	'C/W
Operation Temperature	-25~+85	r
Storage Temperature Range	-55~150	Ċ
ESD	>2	KV
	Power Supply Voltage Logical Input Voltage R/G/B Channel Output Port Withstand Voltage Max clamp current Thermal Resistance Operation Temperature Storage Temperature Range	Power Supply Voltage -0.4~5.5 Logical Input Voltage -0.4~VDD+0.4 R/G/B Channel Output Port Withstand Voltage 30 Max clamp current 20 Thermal Resistance 130 Operation Temperature -25~+85 Storage Temperature Range -55~150

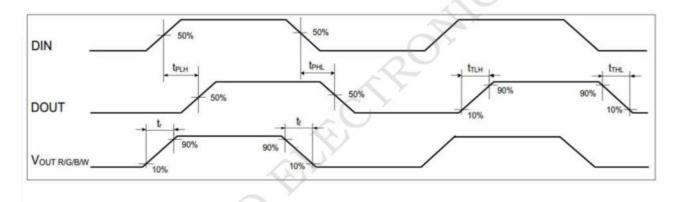
Absolute Maximum Ratings (TA=25°C, VSS=0V, unless otherwise noted.)

Electrical Characteristics (Ta=-20~+70°C, VDD=4.8~5.5V, Vss=0V)

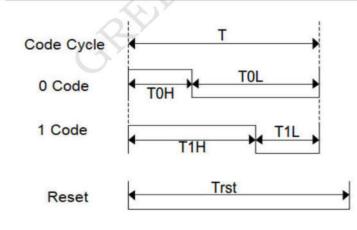
Symbol	Parameter	Test Conditions	Min	Тру	Max	Unit
VDD	Internal Clamp Voltage	External power supply VCC=12V, between VCC and VDD Current limiting resistor RD =1KΩ	4.8	5.2	5.5	v
	Power Voltage	VCC≤5V	3.0	-	5.0	v
loo	Static Current	VDD = 4. 5V, lour "OFF"	-	1.2	-	mA
VH	Input signal	DIN Input High Level	0.7xVDD		-	v
VL	theoshold voltage	DIN Input Low Level	-		0.3×VDD	v
Іон	Dout output current	DOUT output is high level, serially connect 10Ω resistor to GND	-	-40	-	mA
IOL	Dout sink current	DOUT output is low, shorted to VDD		40	-	mA
VDS_S	OUT R/G/B/W Inflection point	lour = 17mA	-	0.8	-	V
%VS.Vps	Q.Y	Iour = 17mA, VDS = 1.0~3.0V	-	0.5		%
%VS.VDD	OUT R/G/B/W current Amout of change	lour =17mA, VDD = 4.5~5.5V	-	0.5	-	%
%VS.TA	Current of Annual of	lout = 17mA, T _A = -40~+85°C	-	5.0	-	%
lieak	Leak current	Vos=26V, lout "OFF"		-	1	uA

Symbol	Parameter	Conditions	Min	Тру	Max	Unit
fрим	OUT R/G/B/W output PWM frequency	IOUT=17mA, OUT port Serially connect 200Ω resistor to VDD		1.2		KHz
TPLH		DOUT port to ground load capacitance 30pF.		85	-	ns
TPHL	Signal transmission delay	Signal transmission delay from DIN to DOUT		70	1	ns
T TLH	DOUT conversion time		•	18	<u>.</u>	ns
TTHL	DOUT conversion time	DOUT port to ground load capacitance 30pF		20	0.	ns
tr	OUT R/G/B/W	IOUT R/G/B/W =17mA, OUT R/G/B/W port serial connection		55	í •	ns
tr	Conversion time	200Ω resistor to VDD, ground load capacitance 15pF	•	75		ns

Switching characteristics(TA=-20~+70°C, VDD=4.8~5.5V, VSS=0V)



Code Description



Data Transfer Time

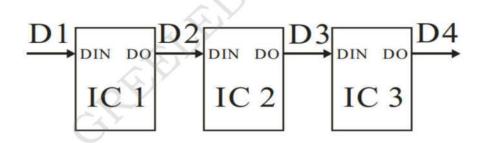
Symbol	Parameter	Min	Тру	Max	Deviation	Unit
т		1.2			±0.05	US
TOH	0 code, high voltage time		0.3		±0.05	US
TOL	0 code, low voltage time	6	0.9	-	±0.05	US
T1H	1 code, high voltage time		0.9		±0.05	US
T1L	1 code, low voltage time		0.3		±0.05	us
Trst	Frame unit, low voltage time	200				us

Composition of 32bit Data

R7 R6	6 R5	R4	R3	R2	R1	RO	G7 G0	B7 B0	W7 W0
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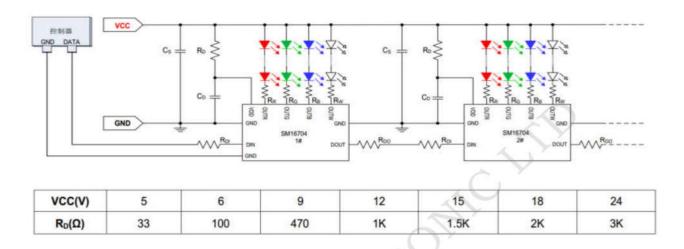
Note: Data transmit in order of RGB, high bit data at first.

Cascade method

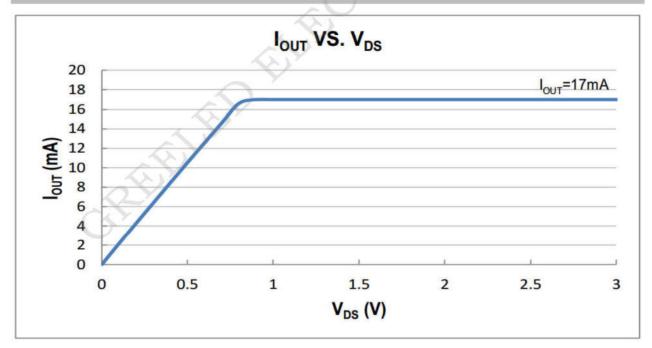


	Trst	第一组32bits数据	第二组32bits数据	第三组32bits数据	Trst	
IC2	Trst		第二组32bits数据	第三组32bits数据	Trst	
C3				第三组32bits数据	Trst	

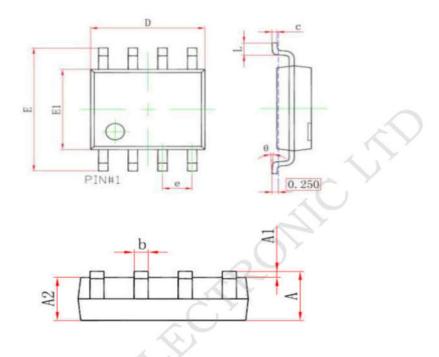
Typical Application Circuit



Constant Current Feature



SOP8



Symbol	Min(mm)	Max(mm)
A	1.25	1.95
A1	· 0.	0.25
A2	1.25	1.75
b	0.25	0.7
•	0.1	0.35
D	4.6	5.3
e	1.27	(BSC)
() E	5.7	6.4
E1	3.7	4.2
L	0.2	1.5
θ	0°	10°

M/N	Packago	QTY		Reel size
141/14	Package	PlasticTube	Reel	Neer Size
SM16704	SOP8	100pcs/tube	4000pcs/reel	13"